

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-8 (Cancelled)

9. (Currently Amended) A method of controlling power to a peripheral device insertable into a host device, the method comprising the steps of:

a) simulating an insertion of the peripheral device with a switch by generating an inserted signal upon extension of an antenna of the peripheral device such that the host device supplies power to the peripheral device, wherein extension of the antenna moves a lever coupled to the switch to generate the inserted signal; and

b) simulating a removal of the peripheral device with the switch by generating a removed signal upon retraction of the antenna of the peripheral device, wherein retraction of the antenna moves the lever such that the switch causes the host device to terminate power [[from]] to the peripheral device and antenna.

10. (Original) The method of claim 9 wherein the switch is in electrical communication with detecting pins of the host device and step (a) further comprises generating the inserted signal on the detecting pin and step (b) further comprises generating the removed signal on the detecting pins.

11. (Cancelled)

12. (Previously presented) The method of claim 10 wherein step (a) comprises generating the inserted signal by forming a low voltage signal on the detecting pin of the host device and step (b) comprises generating the removed signal by forming an open circuit on the detecting pin of the host device.
13. (Original) The method of claim 12 wherein the low voltage signal is a ground potential.
14. (Cancelled)
15. (Cancelled)
16. (Currently Amended) A power control for a peripheral device insertable within a host device, the power control comprising:
- means for simulating an insertion of the peripheral device into host device upon extension of an antenna of the peripheral device;
 - means for simulating a removal of the peripheral device from host device upon retraction of the antenna of the peripheral device; and
 - a lever configured to detect ~~the position~~ positioning of the antenna between the extended and retracted positions, wherein the host device is capable of powering the peripheral device when the antenna is extended and wherein the host device does not power the peripheral device and the antenna when the antenna is retracted.

17. (Original) The power control of claim 16 wherein the means for simulating insertion and the means for simulating removal is a switch.
18. (Original) The power control of claim 17 wherein the switch is operative to generate a signal simulating the removal and insertion of the peripheral device.
19. (Original) The power control of claim 18 wherein the switch is in electrical communication with a detecting pin of the host device and the switch is operative to generate the signal on the detecting pin.
20. (Original) The power control of claim 19 wherein the switch is operative to generate an inserted signal simulating the insertion of the peripheral device and a removed signal simulating the removal of the peripheral device.
21. (Original) The power control of claim 20 wherein the switch is operative to generate an open circuit as the removed signal and a low voltage level as the inserted signal.
22. (Original) The power control of claim 21 wherein the low voltage level is a ground potential.
23. (Original) The power control of claim 22 wherein the switch detects the position of the antenna in order to generate the inserted and removed signals.
24. (Cancelled)

25. (New) A peripheral device adapted to be insertable into a host device comprising:
a body adapted to be selectively removable from the host device;
an antenna coupled to the body and moveable between a first position and a second position;
a circuit within the body and configured to sense positioning of the antenna in the first or second position, the circuit configured to provide an inserted signal to the host device when the antenna is in the first position such that the host device provides power to the peripheral device in response to the inserted signal, the circuit configured to generate a removed signal to the host device when the antenna is in the second position such that the host device does not provide power to the peripheral device and the antenna in response to the removed signal.
26. (New) The peripheral device of claim 25 wherein the circuit is electrically connected to detecting pins of the host device to simulate to the host device whether the peripheral device is inserted or removed from the host device.
27. (New) The peripheral device of claim 25 wherein the circuit is operative to generate an open circuit as the removed signal and a low voltage level as the inserted signal.
28. (New) The peripheral device of claim 27 wherein the low voltage level is a ground potential.
29. (New) The peripheral device of claim 25 wherein the peripheral device is a PCMCIA card.